



20V/5A N-Channel Enhancement Mode MOSFET

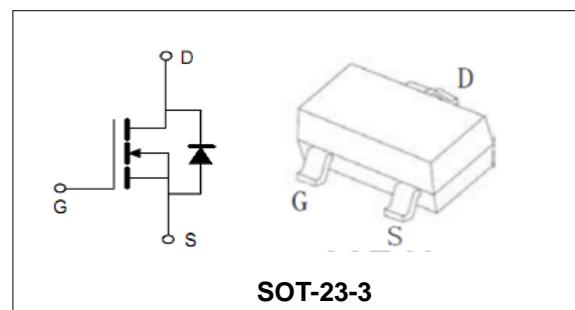
Features

- Advanced trench process technology
- High Density Cell Design For Ultra Low On-Resistance

BVDSS	20	V
ID	5	A
RDS(on)@VGS=4.5V	14	mΩ
RDS(on)@VGS=2.5V	18	mΩ
RDS(on)@VGS=1.8V	28	mΩ

Applications

- Low Side Load Switch
- Battery Switch
- Optimized for Power Management Applications for Portable Products, such as Aeromodelling, Power bank, Brushless motor, Main board , and Others

**Order Information**

Product	Package	Marking	Reel Size	Reel	Carton
PT2312	SOT-23-3	AE9T	7inch	3000PCS	180000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
V _{(BR)DSS}	Drain-Source Breakdown Voltage	20	V	
V _{GS}	Gate-Source Voltage	±8	V	
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
I _S	Diode Continuous Forward Current	TA =25°C	5	A
Mounted on Large Heat Sink				
I _{DM}	Pulse Drain Current Tested (Sillicon Limit) (Note1)	TA =25°C	15	A
I _D	Continuous Drain current	TA =25°C	5	A
P _D	Maximum Power Dissipation	TA =25°C	0.75	W
R _{θJA}	Thermal Resistance Junction-to-Ambient (Note2)		140	°C/W

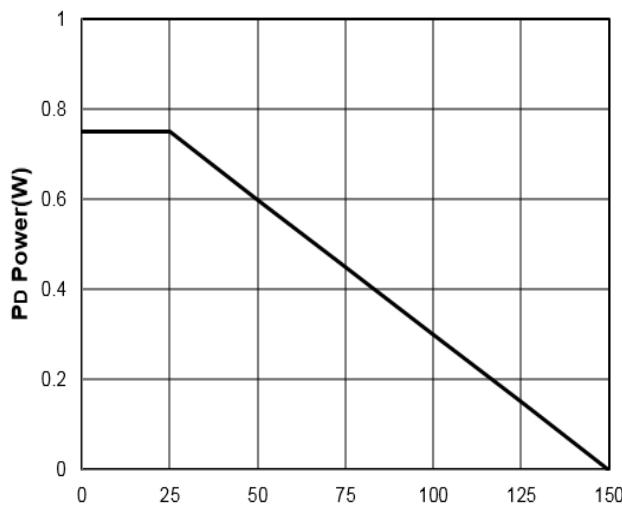
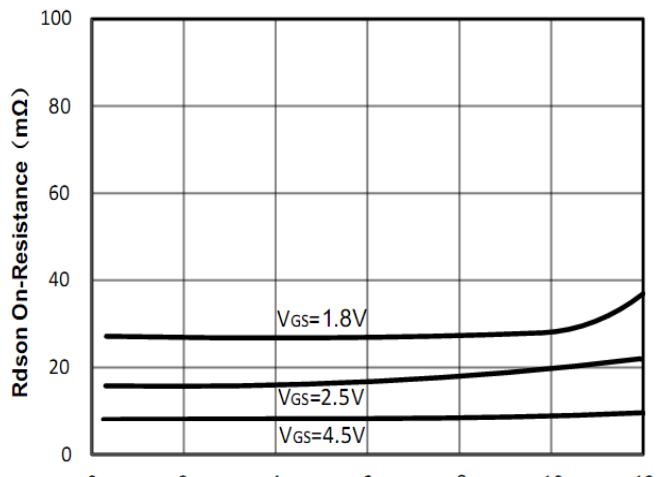
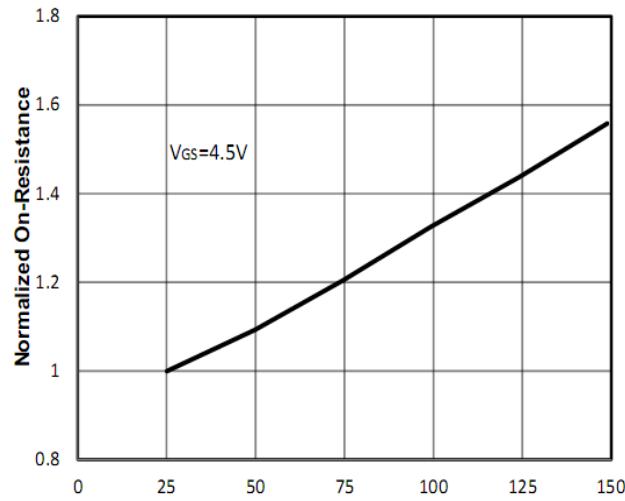
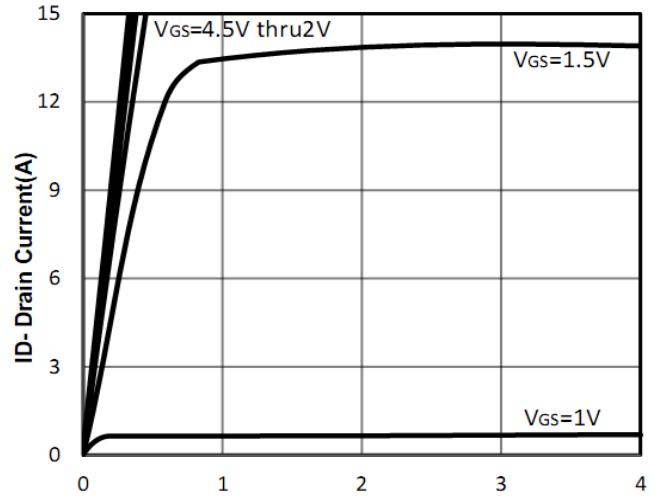
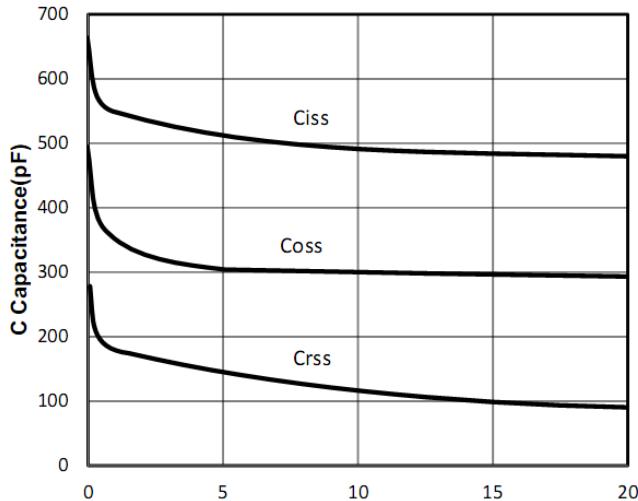
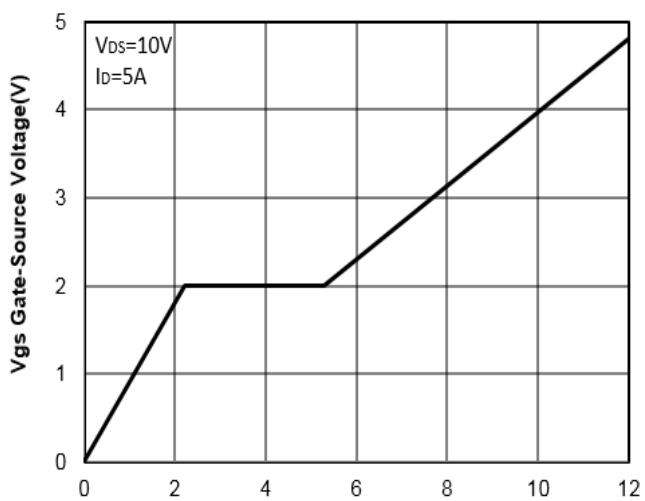


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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$V_{GS}=0V, ID=250\mu A$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$V_{DS}=20V, V_{GS}=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 8V, V_{DS}=0V$	--	--	± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, ID=250\mu A$	0.4	--	1	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$V_{GS}=4.5V, ID=5A$	--	14	28	$m\Omega$
		$V_{GS}=2.5V, ID=4.5A$	--	18	35	$m\Omega$
		$V_{GS}=1.8V, ID=4A$		28	42	$m\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ C$ (unless otherwise stated) (Note4)						
C_{iss}	Input Capacitance	$V_{DS}= 8V, V_{GS}=0V, F=1MHz$	--	500	--	pF
C_{oss}	Output Capacitance		--	300	--	pF
C_{rss}	Reverse Transfer Capacitance		--	140	--	pF
Q_g	Total Gate Charge	$V_{DS}= 10V, ID= 5A, V_{GS}= 4.5V$	--	11.2	--	nC
Q_{gs}	Gate-Source Charge		--	1.4	--	nC
Q_{gd}	Gate-Drain Charge		--	2.2	--	nC
Switching Characteristics (Note4)						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=10V, ID=1A, RG=6\Omega, V_{GS}=4.5V$	--	15	--	nS
t_r	Turn-on Rise Time		--	40	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	48	--	nS
t_f	Turn-off Fall Time		--	31	--	nS
Source- Drain Diode Characteristics@ $T_J = 25^\circ C$ (unless otherwise stated)						
V_{SD}	Forward on voltage (Note3)	$I_S=1.8A, V_{GS}=0V$	--	--	1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec
3. Pulse Test: pulse width ≤ 300 us, duty cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: T_J Junction Temperature (°C)

Figure2: I_D Drain Current (A)

Figure3: T_J Junction Temperature (°C)

Figure4: V_{DS} Drain-Source Voltage (V)

Figure5: V_{DS} Drain-Source Voltage (V)

Figure6: Q_g Gate Charge (nC)

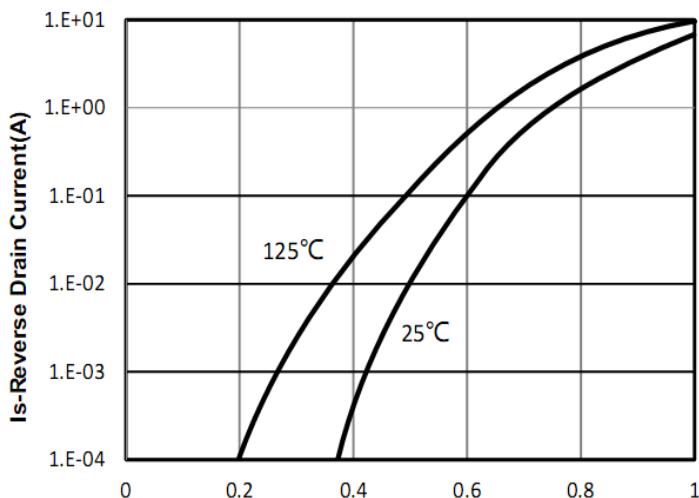
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Figure 7: V_{sd} Source-Drain Voltage (V)

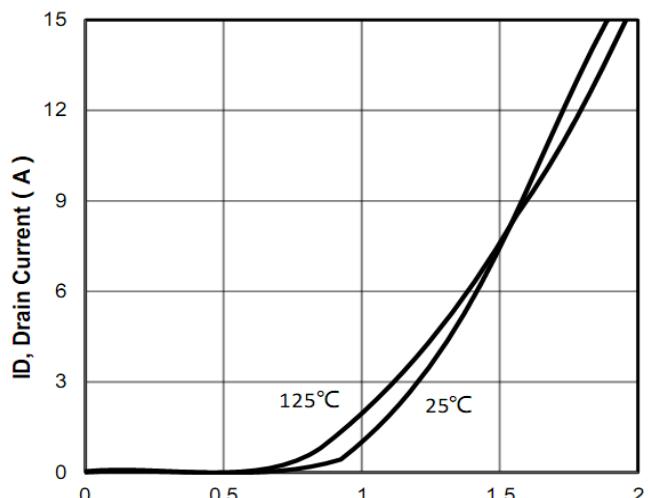


Figure 8: V_{gs} Gate-Source Voltage (V)

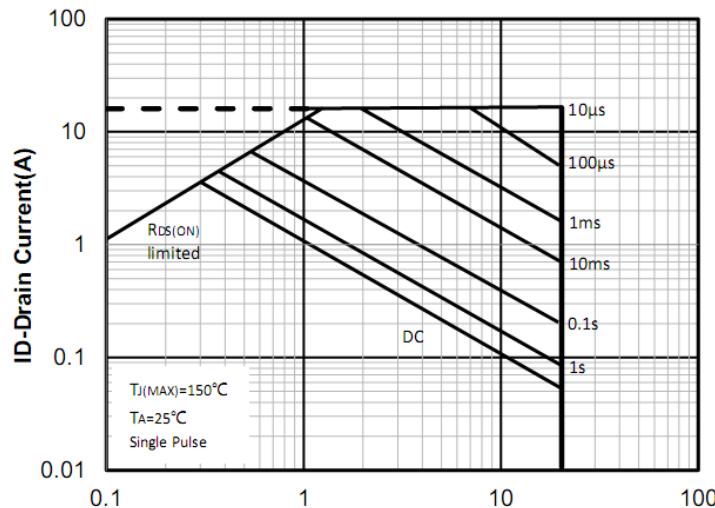


Figure 9: V_{ds} Drain -Source Voltage (V)

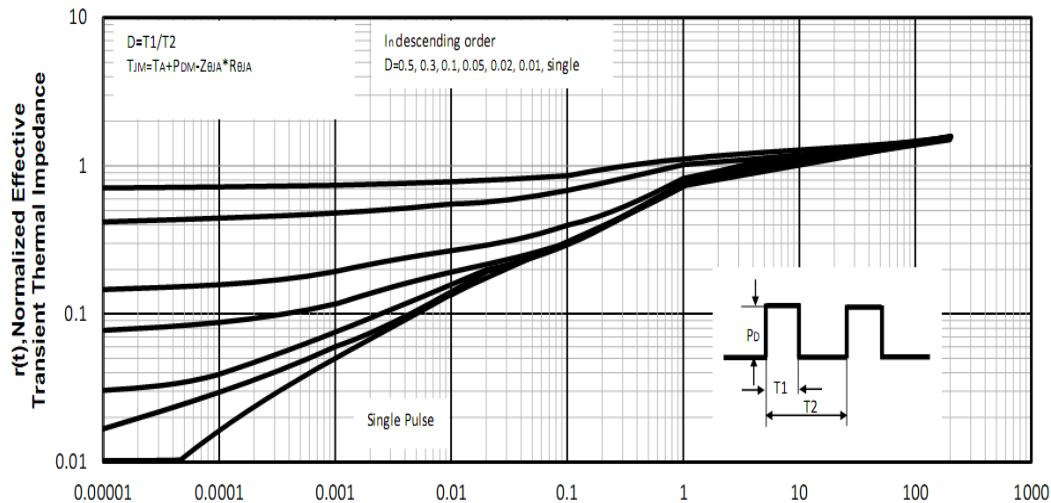
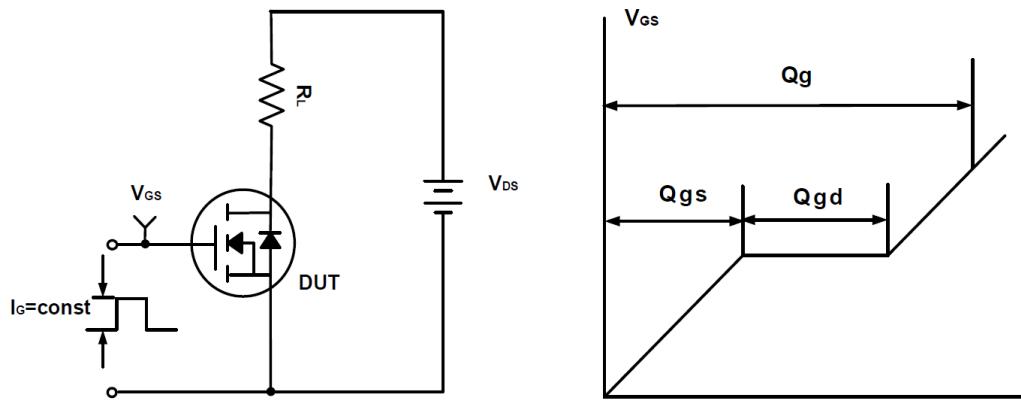
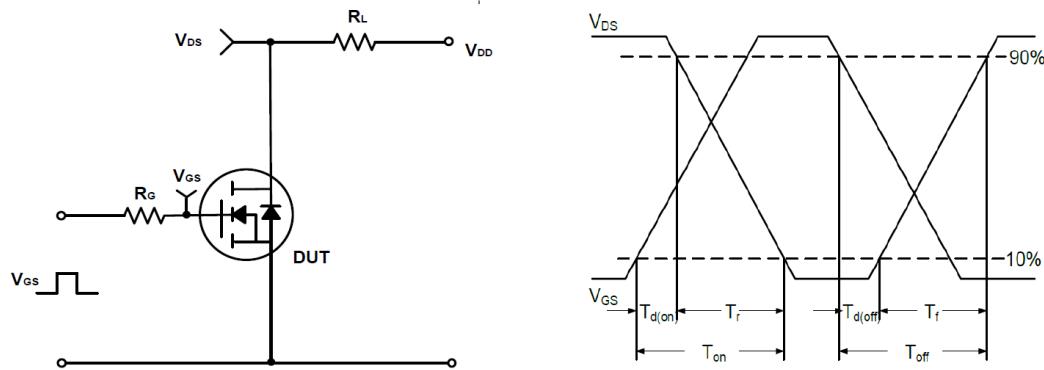
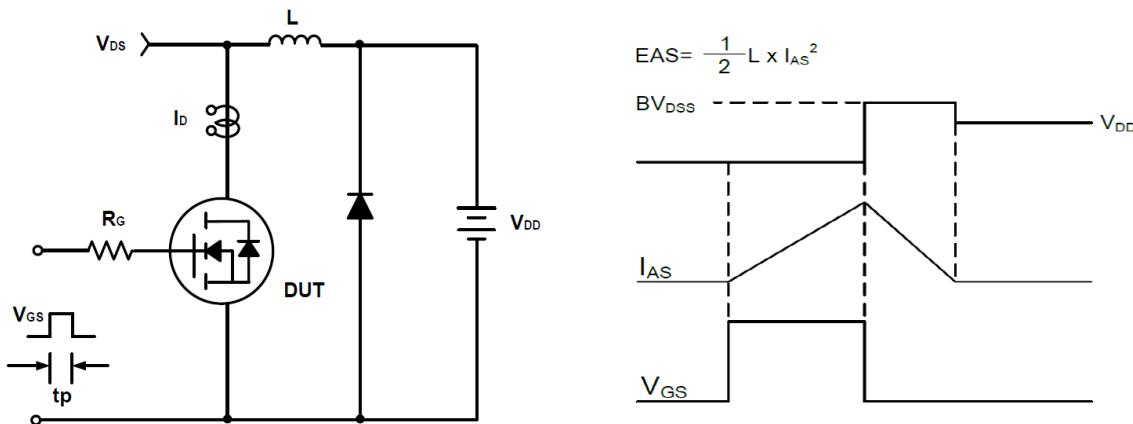
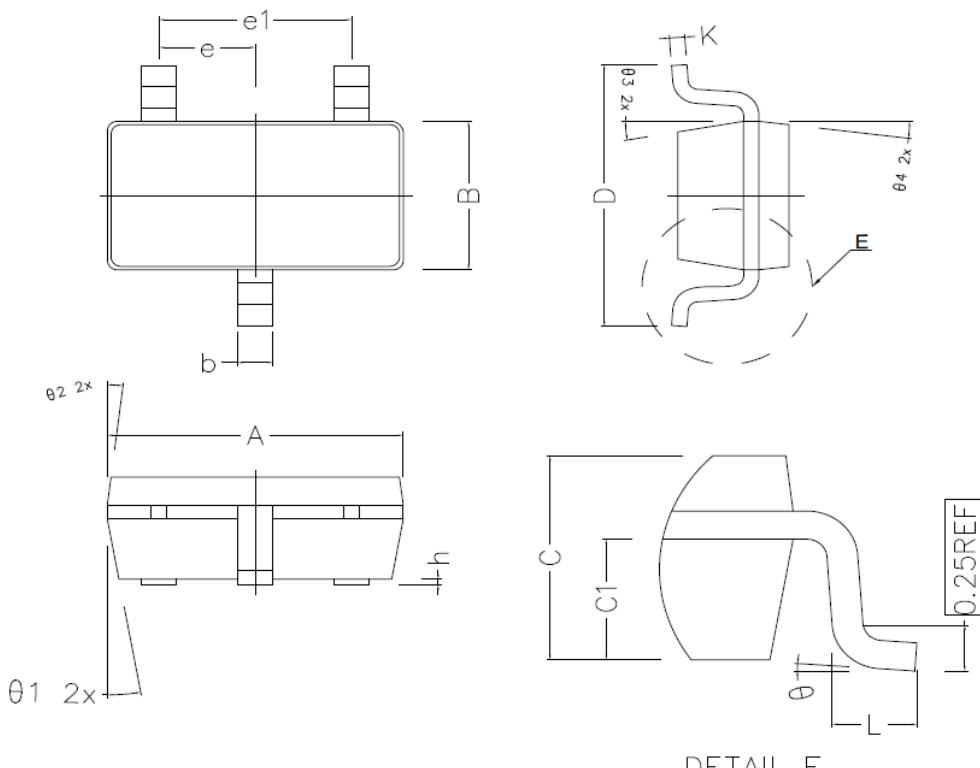


Figure 10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

SOT-23-3 Package Outline Dimensions (Units: mm)


COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	2.820	2.920	3.020
B	1.500	1.600	1.700
C	1.050	1.100	1.150
C1	0.600	0.650	0.700
D	2.650	2.800	2.950
L	0.300	0.450	0.600
b	0.280	0.350	0.420
h	0.020	0.050	0.100
K	0.120	—	0.230
e	0.950TYPE		
e1	1.900TYPE		
θ ₁	10° TYPE		
θ ₂	7° TYPE		
θ ₃	10° TYPE		
θ ₄	7° TYPE		
θ	0° ~ 8°		