



65V/88A N-Channel Advanced Power MOSFET

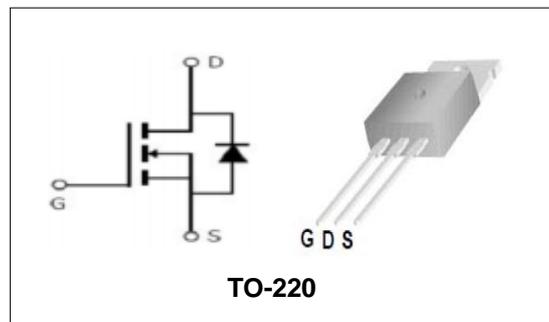
Features

- Improved dv/dt Capability, High Ruggedness.
- Maximum Junction Temperature Range (150°C)
- 100% Avalanche Tested

BVDSS	65	V
ID	88	A
RDSON@VGS=10V	6.4	mΩ

Applications

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)



Order Information

Product	Package	Marking	Tube	Carton
PTP88N07	TO-220	PTP88N07	50PCS	5000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	65	V	
V_{GS}	Gate-Source Voltage	±25	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_S	Diode Continuous Forward Current	TC =25°C	80	A
Mounted on Large Heat Sink				
E_{AS}	Single Pulse Avalanche Energy (Note1)	288	mJ	
I_{DM}	Pulse Drain Current Tested (Silicon Limit) (Note2)	TC =25°C	352	A
I_D	Continuous Drain current	TC =25°C	88	A
P_D	Maximum Power Dissipation	TC =25°C	174	W
$R_{\theta Jc}$	Thermal Resistance Junction-to-Case (Note3)	0.72	°C/W	
$R_{\theta JA}$	Thermal Resistance Junction-to-Ambient (Note3)	62.5	°C/W	

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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain- Source Breakdown Voltage	VGS=0V ID=250μA	65	--	--	V
I _{DSS}	Zero Gate Voltage Drain current	VDS=65V,VGS=0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	VGS=±25V,VDS=0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	VDS=VGS,ID=250μA	2	3	4	V
R _{DS(ON)}	Drain-Source On-State Resistance (Note4)	VGS=10V, ID=40A	--	6.4	7.8	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated) (Note5)						
C _{iss}	Input Capacitance	VDS=30V,	--	3000	--	pF
C _{oss}	Output Capacitance	VGS=0V,	--	430	--	pF
C _{rss}	Reverse Transfer Capacitance	F=1MHz	--	250	--	pF
Q _g	Total Gate Charge	VDS=30V	--	76	--	nC
Q _{gs}	Gate-Source Charge	ID=40A	--	14	--	nC
Q _{gd}	Gate-Drain Charge	VGS=10V	--	25	--	nC
Switching Characteristics (Note5)						
t _{d(on)}	Turn-on Delay Time	VDD=30V,	--	17	--	nS
t _r	Turn-on Rise Time	ID=40A,	--	15	--	nS
t _{d(off)}	Turn-off Delay Time	VGS=10V,	--	62	--	nS
t _f	Turn-off Fall Time	RG=6Ω	--	32	--	nS
Source- Drain Diode Characteristics @ T_J = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	IS=40A,VGS=0V	--	--	1.3	V

Note:

- Limited by T_{Jmax}, starting T_J = 25° C, R_G = 25Ω, V_D =50V, VGS =10V. Part not recommended for use above this value.
- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board, t ≤ 10 sec.
- Pulse Test: pulse width ≤ 300 us, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.



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Typical Characteristics

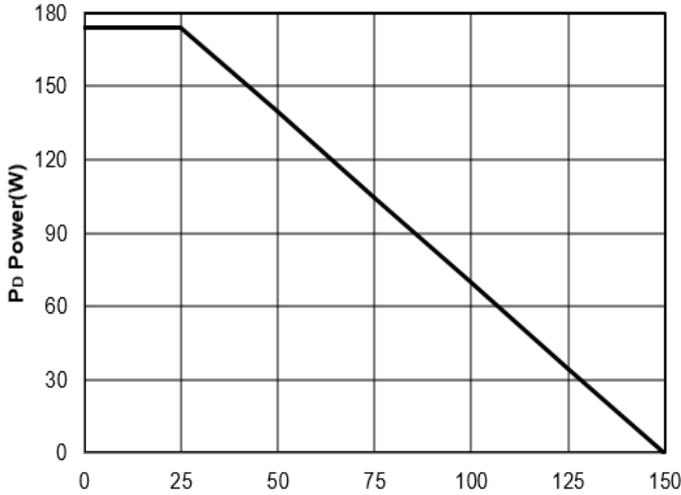


Figure1: T_j Junction Temperature (°C)

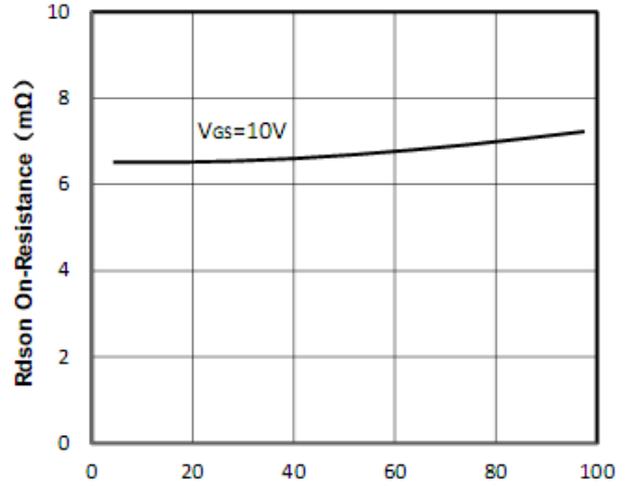


Figure2: I_d Drain Current (A)

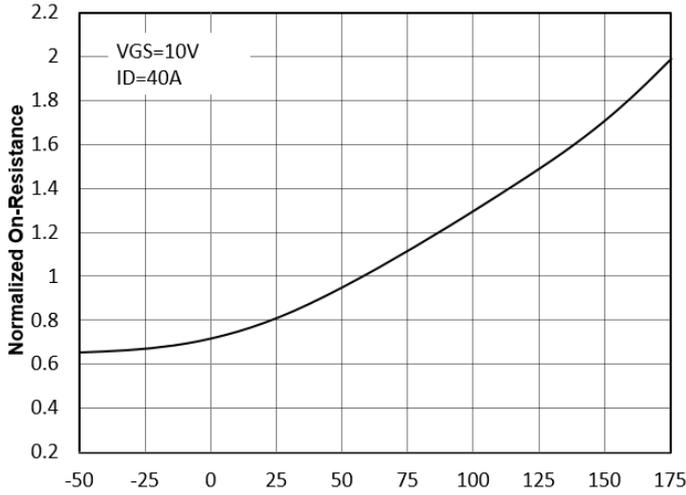


Figure3: T_j Junction Temperature (°C)

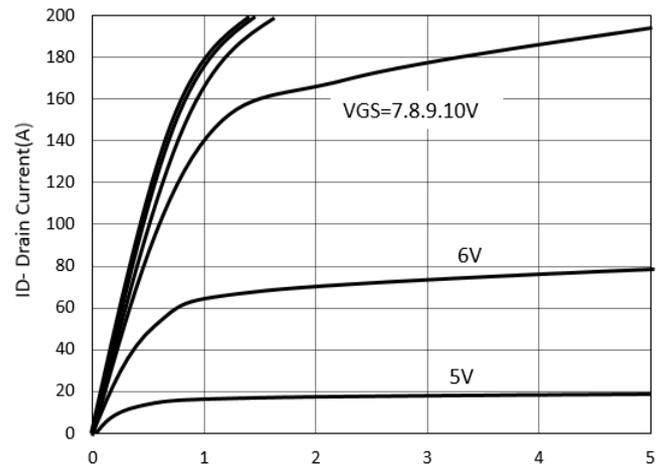


Figure4: V_{ds} Drain-Source Voltage (V)

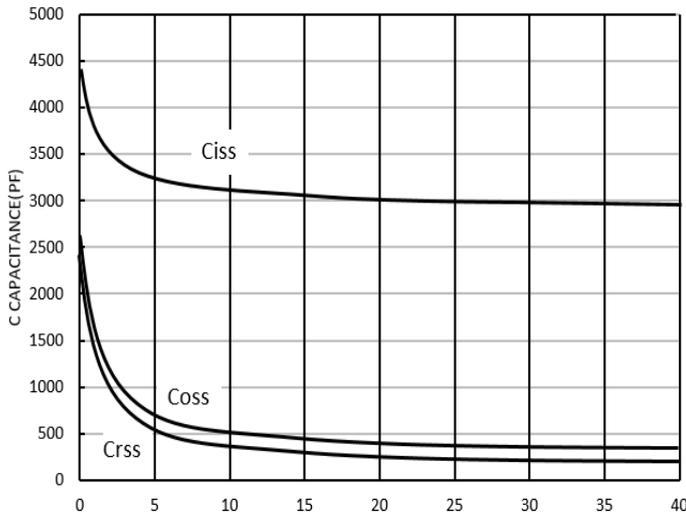


Figure5: V_{ds} Drain-Source Voltage (V)

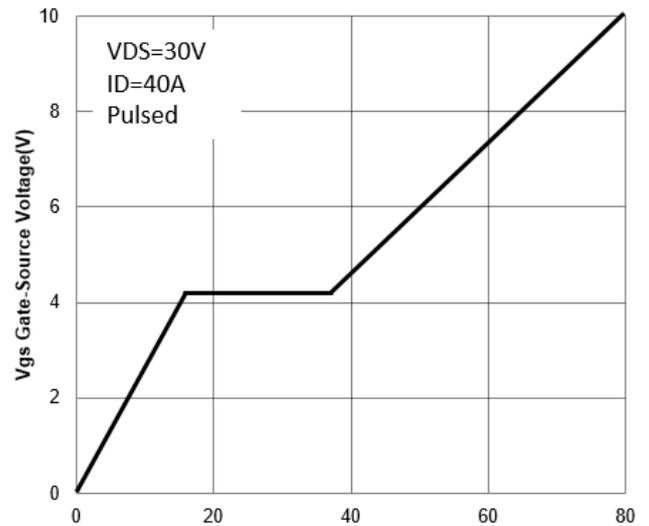


Figure6: Q_g Gate Charge (nC)



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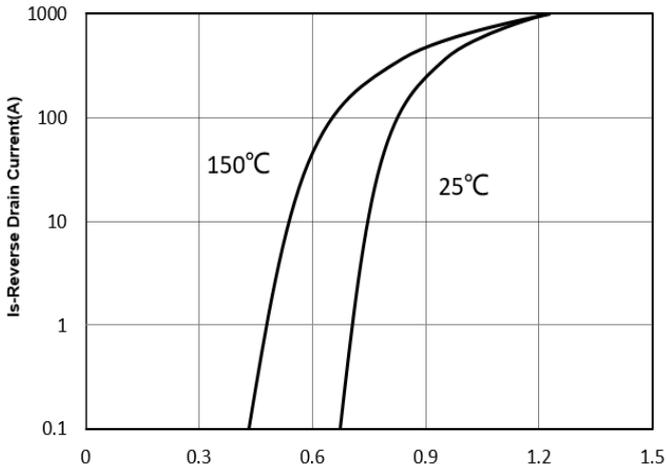


Figure7: Vsd Source-Drain Voltage (V)

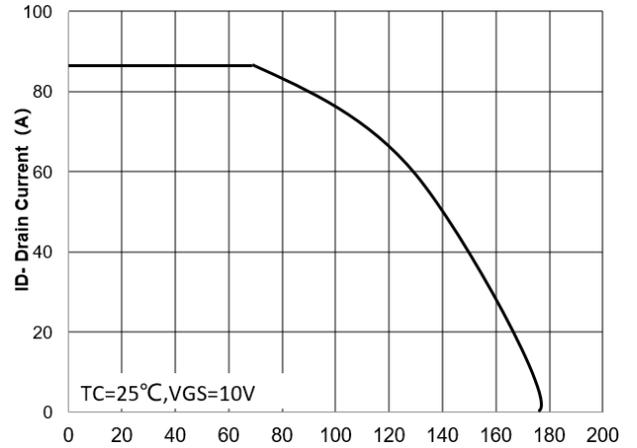


Figure8: Vgs Gate-Source Voltage (V)

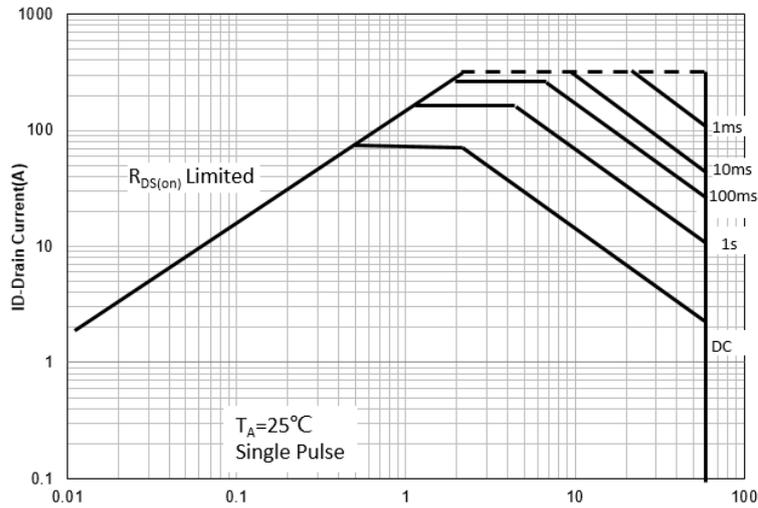


Figure9: VDS Drain -Source Voltage (V)

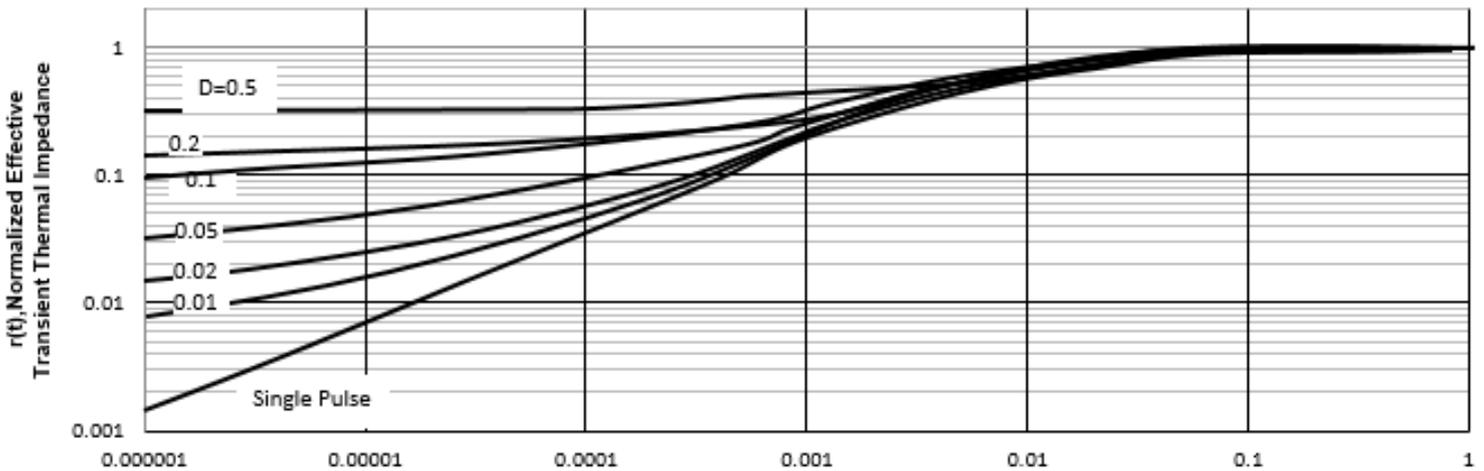


Figure10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform:

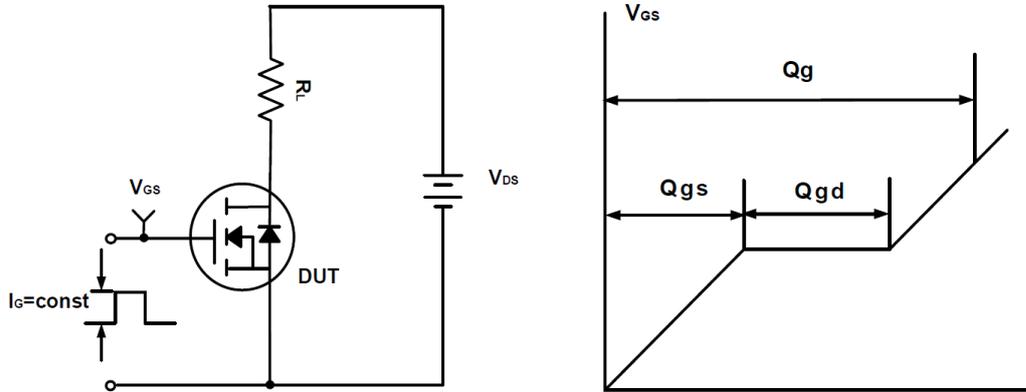


Figure A Gate Charge Test Circuit & Waveforms

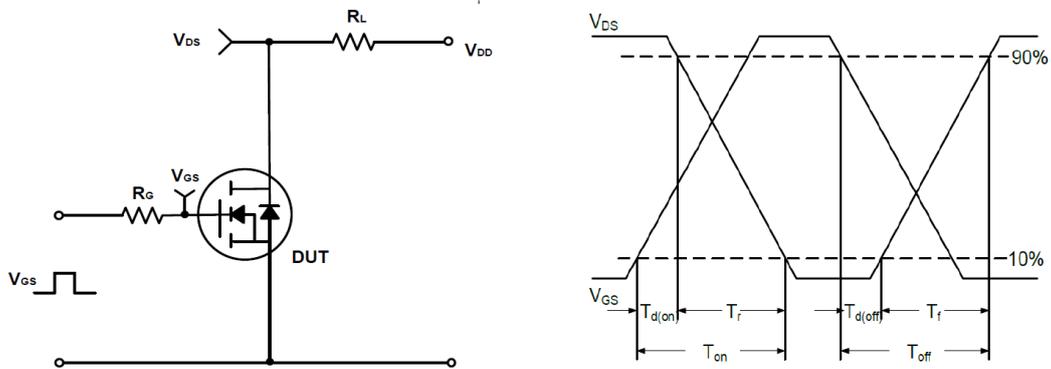


Figure B Switching Test Circuit & Waveforms

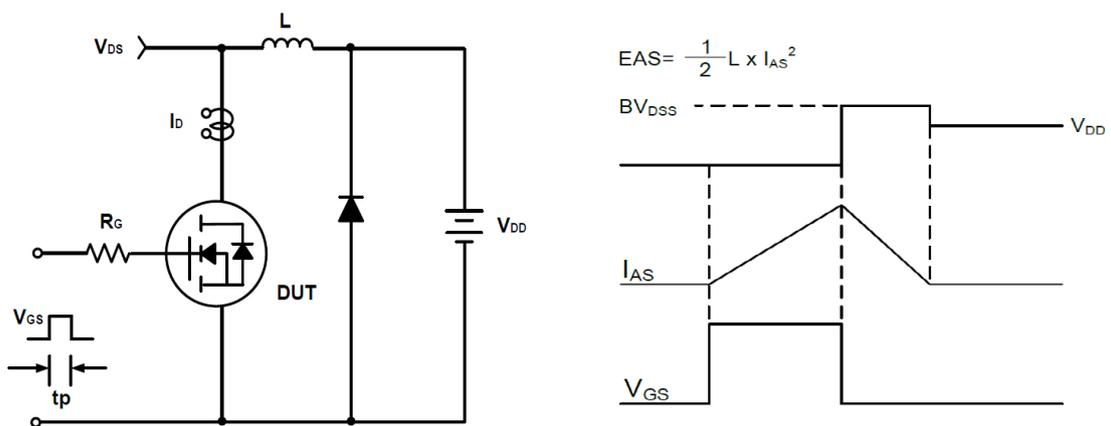
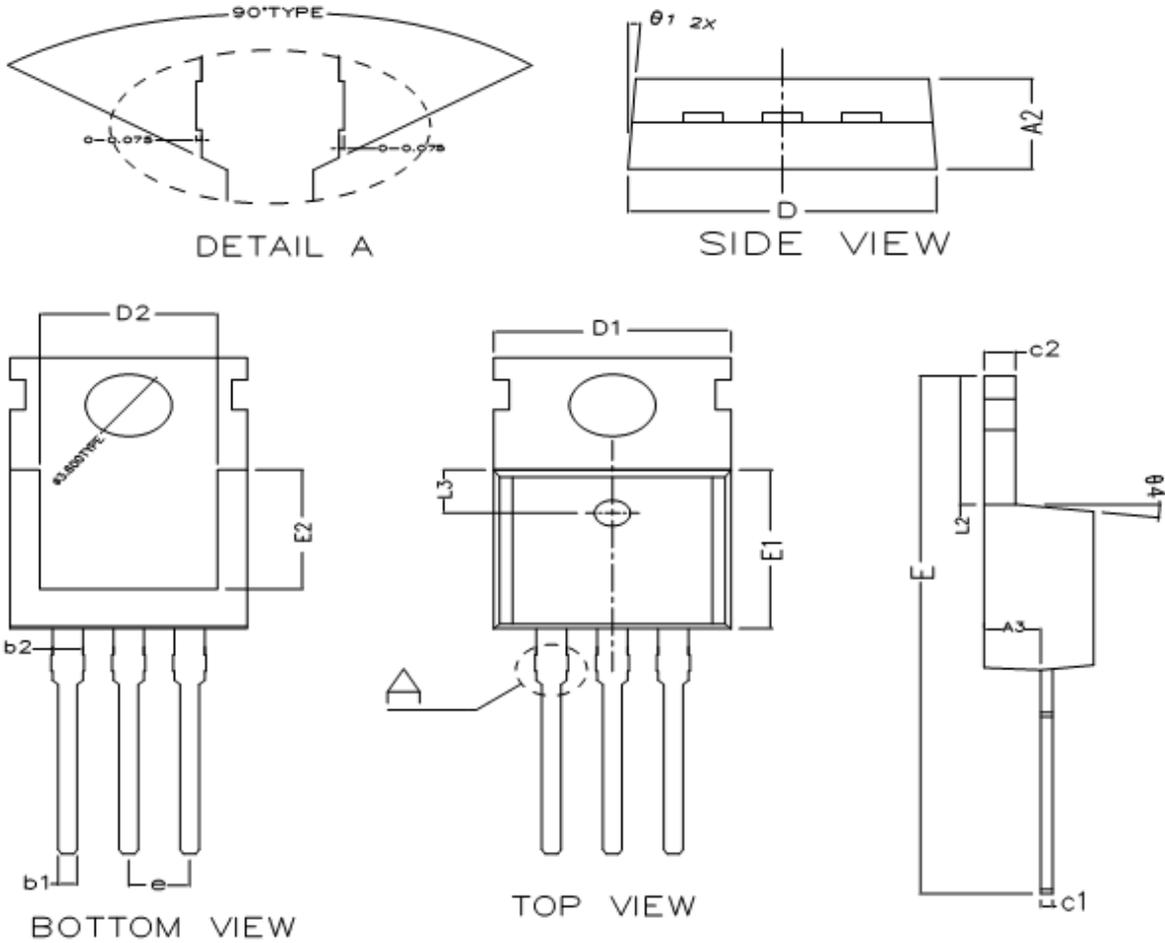


Figure C Unclamped Inductive Switching Circuit & Waveforms



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TO-220 Package Outline Dimensions (Units: mm)



COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A2	4.470	4.570	4.670
A3	2.300	2.350	2.400
b1	0.750	0.800	0.850
b2	1.27 TYPE		
c1	0.450	0.500	0.550
c2	1.250	1.300	1.380
D	9.900	10.000	10.100
D1	10.000TYPE		
D2	8.000TYPE		
E	28.660	28.860	29.060
E1	9.000	9.100	9.200
E2	7.000TYPE		
e	2.540TYPE		
L2	6.350	6.500	6.650
L3	2.50TYPE		
θ1	3° TYPE		
θ2	3° TYPE		
θ3	7° TYPE		
θ4	7° TYPE		