

20V/17A N-Channel Advanced Power MOSFET

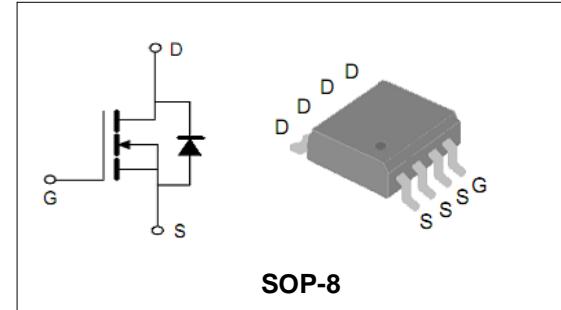
Features

- Very Low RDS(on) @ 3.3V Logic.
- 3.3V Logic Level Control
- SOP8 Package

BVDSS	20	V
ID	17	A
RDSON@VGS=4.5V	6.2	mΩ
RDSON@VGS=2.5V	7.6	mΩ

Applications

- Low Side Load Switch
- Battery Switch
- Optimized for Power Management Applications for Portable Products, such as Aeromodelling, Power bank, Brushless motor, Main board , and Others



Order Information

Product	Package	Marking	Reel Size	Reel	Carton
PTS2017	SOP-8	PTS2017	13inch	3000PCS	48000PCS

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	20	V	
V_{GS}	Gate-Source Voltage	± 8	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_S	Diode Continuous Forward Current	TA =25°C	17	A
Mounted on Large Heat Sink				
I_{DM}	Pulse Drain Current Tested (Sillicon Limit) (Note1)	TA =25°C	68	A
I_D	Continuous Drain current	TA =25°C	17	A
P_D	Maximum Power Dissipation	TA =25°C	2	W
$R_{θJA}$	Thermal Resistance Junction-to-Ambient (Note2)		65	°C/W

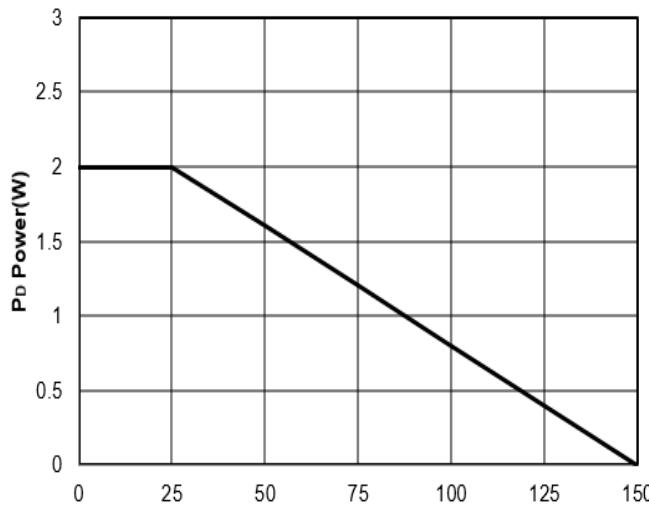
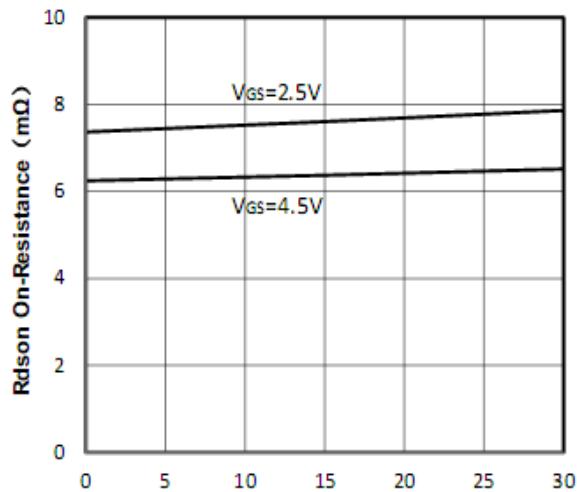
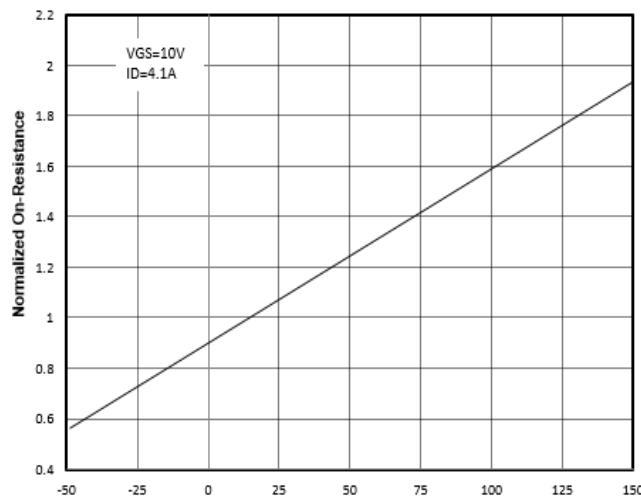
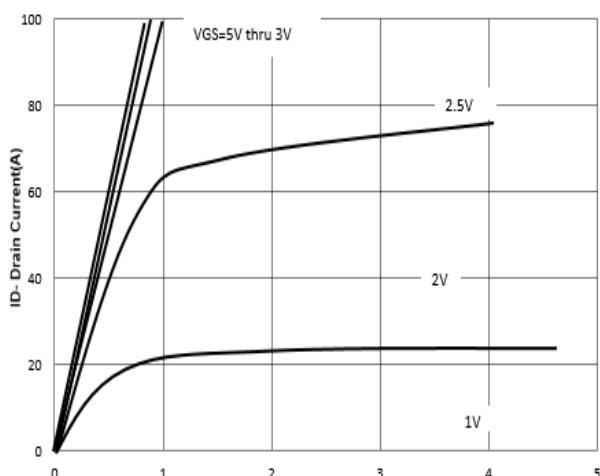
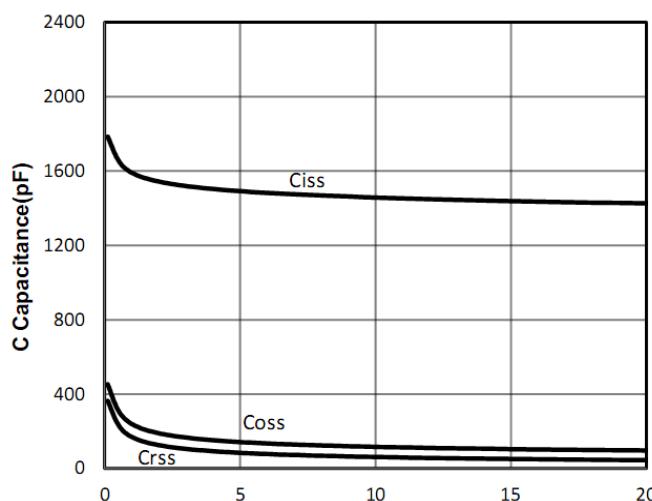
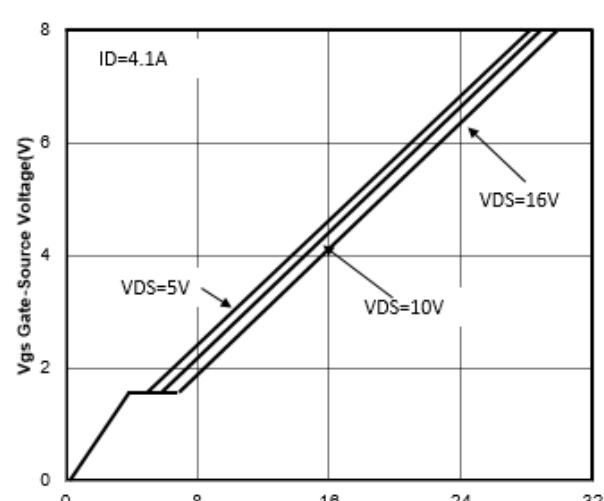


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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain- Source Breakdown Voltage	$VGS=0V$ $ID=250\mu A$	20	--	--	V
I_{DSS}	Zero Gate Voltage Drain current	$VDS=16V$, $VGS=0V$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$VGS=\pm 8V$, $VDS=0V$	--	--	± 10	μA
$V_{GS(TH)}$	Gate Threshold Voltage	$VDS=VGS$, $ID=250\mu A$	0.45	0.7	1.2	V
$R_{DS(ON)}$	Drain-Source On-State Resistance (Note3)	$VGS=4.5V$, $ID=15A$	--	6.2	7.5	$m\Omega$
		$VGS=2.5V$, $ID=15A$	--	7.6	9	$m\Omega$
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated) (Note4)						
C_{iss}	Input Capacitance	$VDS=5V$, $VGS=0V$, $F=1MHz$	--	1550	--	pF
C_{oss}	Output Capacitance		--	140	--	pF
C_{rss}	Reverse Transfer Capacitance		--	105	--	pF
Q_g	Total Gate Charge	$VDS=5V$, $ID=10A$, $VGS=4.5V$	--	58	--	nC
Q_{gs}	Gate-Source Charge		--	18	--	nC
Q_{gd}	Gate-Drain Charge		--	12	--	nC
Switching Characteristics (Note4)						
$t_{d(on)}$	Turn-on Delay Time	$VDS=10V$, $ID=3A$, $RG=4.7\Omega$, $VGS=4.5V$	--	20	--	nS
t_r	Turn-on Rise Time		--	12	--	nS
$t_{d(off)}$	Turn-off Delay Time		--	25	--	nS
t_f	Turn-off Fall Time		--	18	--	nS
Source- Drain Diode Characteristics@ TJ = 25°C (unless otherwise stated)						
V_{SD}	Forward on voltage	$IS=10A$, $VGS=0V$	--	0.8	1.2	V

Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: pulse width ≤ 300 us, duty cycle $\leq 2\%$.
4. Guranteed by design, not subject to production testing.

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Typical Characteristics

Figure1: TJ Junction Temperature (°C)

Figure2: Id Drain Current (A)

Figure3: TJ Junction Temperature (°C)

Figure4: Vds Drain-Source Voltage (V)

Figure5: Vds Drain-Source Voltage (V)

Figure6: Qg Gate Charge (nC)

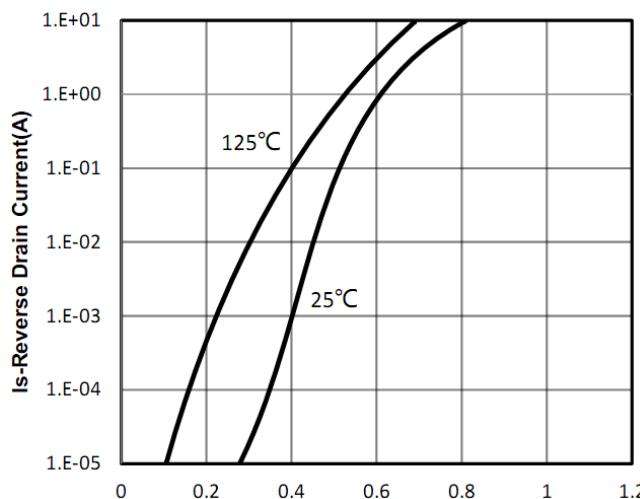
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Figure 7: V_{sd} Source-Drain Voltage (V)

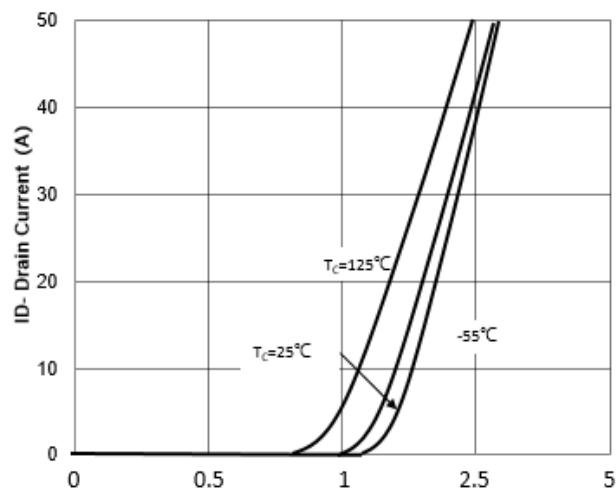


Figure 8: V_{gs} Gate-Source Voltage (V)

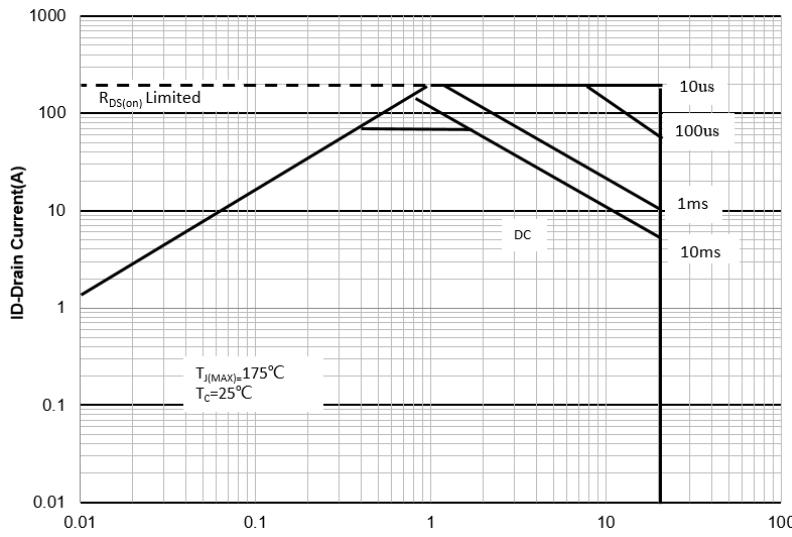


Figure 9: V_{sd} Drain -Source Voltage (V)

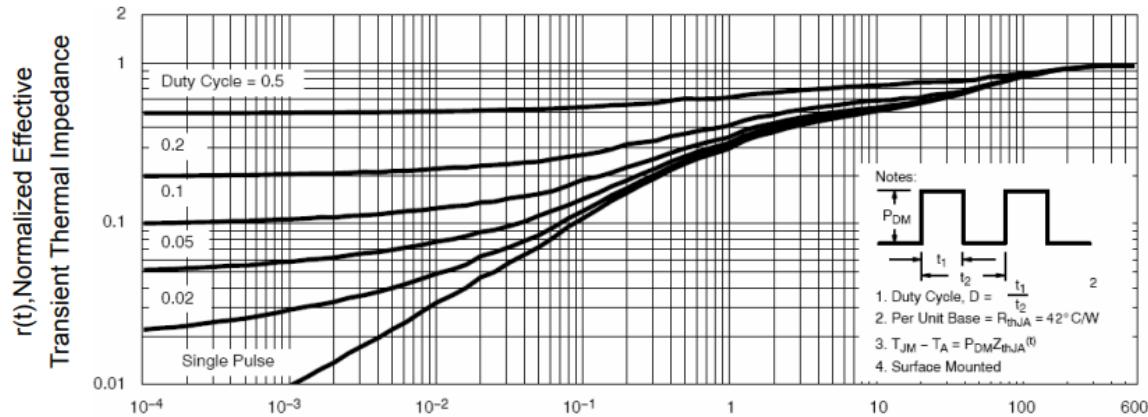
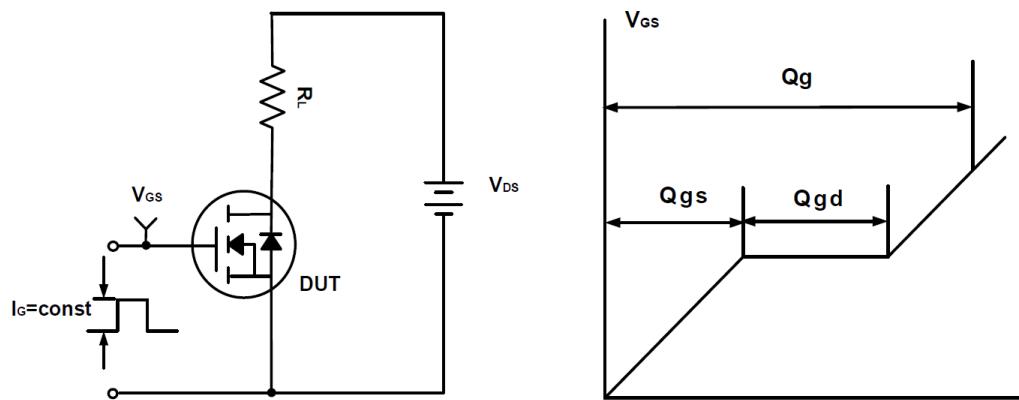
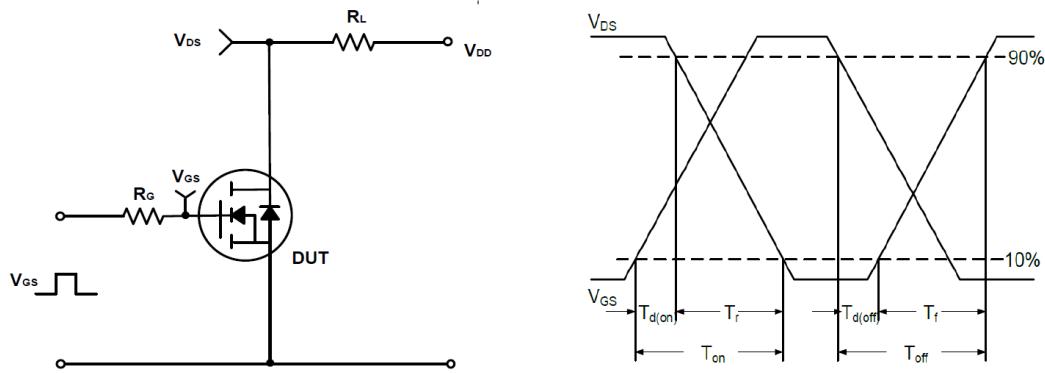
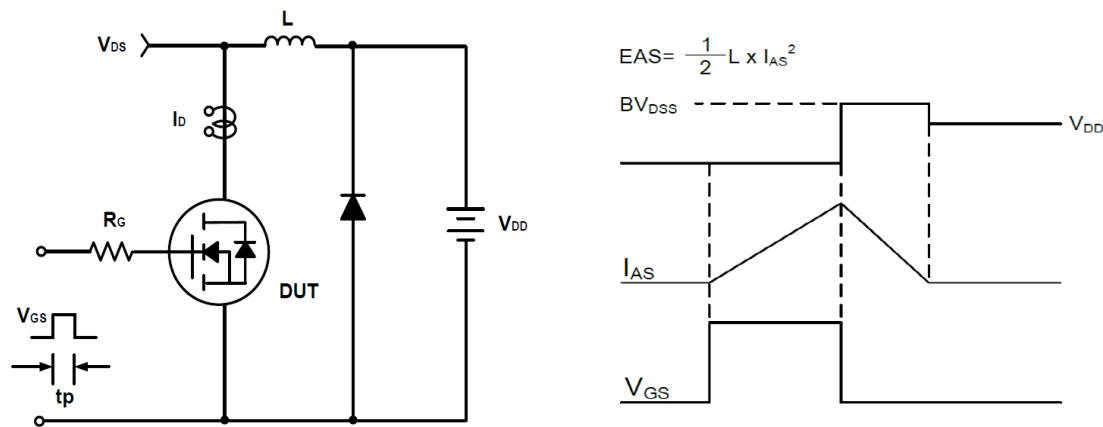
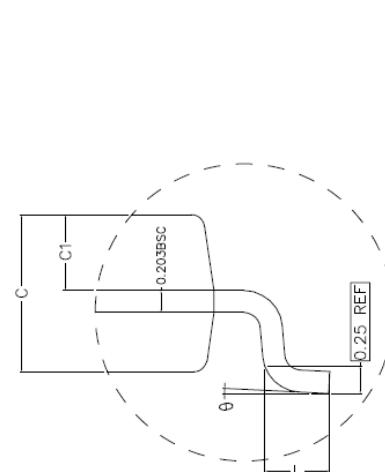
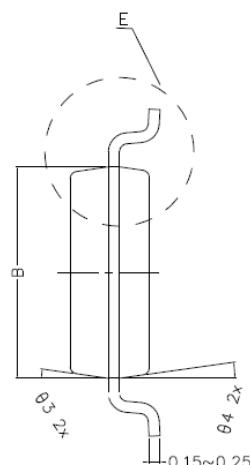
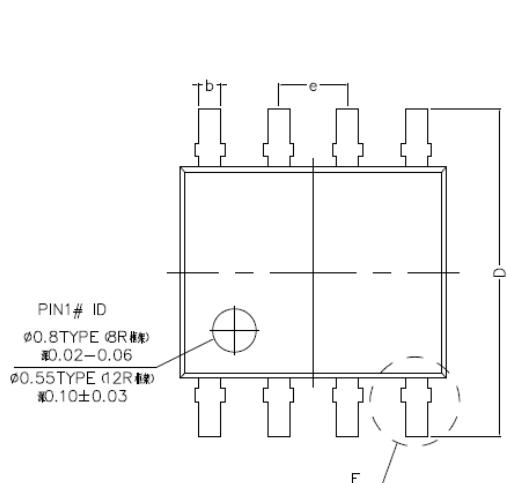
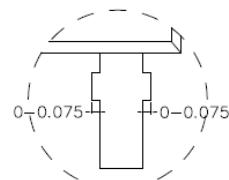
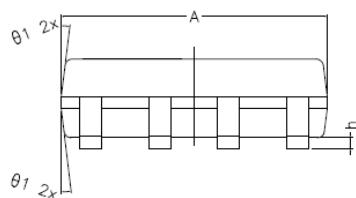


Figure 10: Square Wave Pulse Duration (sec)

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Test Circuit and Waveform:

Figure A Gate Charge Test Circuit & Waveforms

Figure B Switching Test Circuit & Waveforms

Figure C Unclamped Inductive Switching Circuit & Waveforms

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SOP-8 Package Outline Dimensions (Units: mm)


DETAIL E



DETAIL F

COMMON DIMENSIONS (UNITS OF MEASURE IS mm)			
	MIN	NORMAL	MAX
A	4.800	4.900	5.000
B	3.800	3.900	4.000
C	1.350	1.450	1.550
C1	0.650	0.700	0.750
D	5.900	6.100	6.300
L	0.500	0.600	0.700
b	0.350	0.400	0.450
h	0.050	0.150	0.250
e	1.270 TYPE		
θ_1	7° TYPE(8R)	12° TYPE(12R)	
θ_2	7° TYPE(8R)	10° TYPE(12R)	
θ_3	8° TYPE(8R)	12° TYPE(12R)	
θ_4	8° TYPE(8R)	10° TYPE(12R)	
θ	$0^\circ \sim 8^\circ$		